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Benchmark of Signal Processing Multiprocessor Architectures IEEE Pacific Rim Conference on Communications, Computers and Signal Processing, Conference Proceedings Proceedings of the IEEE 1986 National Aerospace and Electronics Conference, NAECON 1986 International Conference on Computer Design (ICCD '99) Proceedings High-performance Computing, National Security Applications, and Export Control Policy at the Close of the 20th Century IEEE ASSP Workshop on Applications of Signal Processing to Audio and Acoustics Signal Processing Workshop on Higher-Order Statistics, June 7-9, 1993, Stanford Sierra Group, South Lake Tahoe, CA, USA. Optical Engineering Benchmarking Microprocessors for High-End Signal Processing Scientific Computing in Object-oriented Parallel Environments IEEE VLSI Test Symposium Applied Signal Processing 1997 IEEE International Conference on Acoustics, Speech, and Signal Processing: Statistical signal and array processing, applications ACM SIGPLAN Notices Statistical and Determinsistic [sic] Power Optimization Techniques Using Multiple Supply and Threshold Voltages Virtual Acoustics and 3-D Sound in Multimedia Signal Processing Distributed and Parallel Computing Higher-order Statistical Signal Processing CPU Performance Evaluation and Execution Time Prediction Using Narrow Spectrum Benchmarking *Brendan Creedon IEEE Computer Society Seymour E. Goodman International Association of Science and Technology for Development Ashish N. Srivastava Jyri Huopaniemi Hesham El-Rewini Boualem Boashash University of California, Berkeley. Computer Science Division*

the proceedings from the october 1999 conference include 107 technical presentations from 14 different countries not restricted to presented papers this volume includes both the keynote and plenary addresses poster presentations as well as the proceedings of two tutorials one on cad and one on benchmarking selecting and debugging microcontrollers topics covered include applied verification techniques computer arithmetic intelligent memory design convergence test generation and delay testing microarchitecture and digital signal processors no subject index annotation copyrighted by book news inc portland or

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there are a number of general purpose microprocessor architectures which while not designed for high end signal processing might provide the processing performance required for complex radars signal intelligence and other demanding applications but how well does each really perform as a digital signal processor to answer this question some simple benchmarks were run on a 1ghz freescale 7447 powerpc 1 8 ghz ibm 970 powerpc 1 8 ghz amd opteron and 800 mhz broadcom mips based 1250 chip the bottom line of this set of benchmarks is that the powerpc with altivec produces impressive computational performance compared to the other processors considered now that ibm is shipping its powerpc 970 with altivec there is a processor alternative that addresses the memory bandwidth limitations of the 7447 yet despite the strengths of altivec the benchmarks revealed that the alternative processors offer some interesting capabilities for particular types of signal processing for example memory bandwidth may be more important than sheer speed or where parts count is a limitation

mathematics of computing parallelism

higher order statistical signal processing brings together some most recent innovations in the field of higher order statistical signal processing it is structured to provide a comprehensive understanding of the fundamentals of the discipline as well as a treatment of recent advances

we have developed tools to measure the performance of a variety of machines from workstations to supercomputers we have also characterized the execution of many large applications including the spec and perfect benchmark suites by merging these machine and program characterizations we can estimate execution times quite accurately for arbitrary machine program combinations another aspect of the research has consisted in characterizing the effectiveness of optimizing compilers another contribution of this dissertation is to propose and investigate new metrics for machine and program similarity and the information that can be derived from them

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